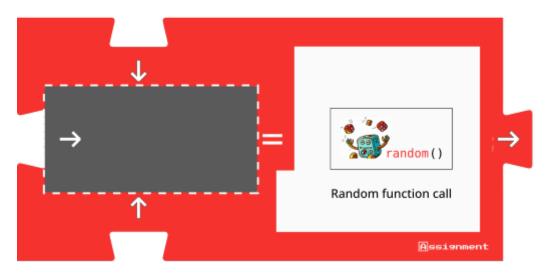


BEGIN block END block

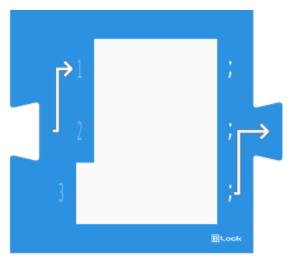


Assignment block

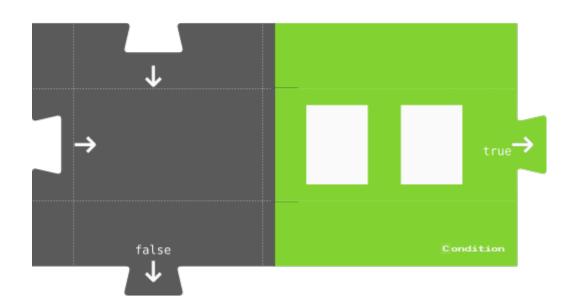


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Code block

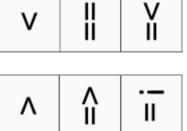


Condition block



	7	4	9	∞	10	12
0	1	3	5	7	6	11
	::	∷	::	•••	•	•
	2	4	9	8	10	12
0	1	\sim	2	7	6	11
	::	::	••	•••	•	•
	2	4	9	8	10	12
0	1	\sim	5	7	6	11

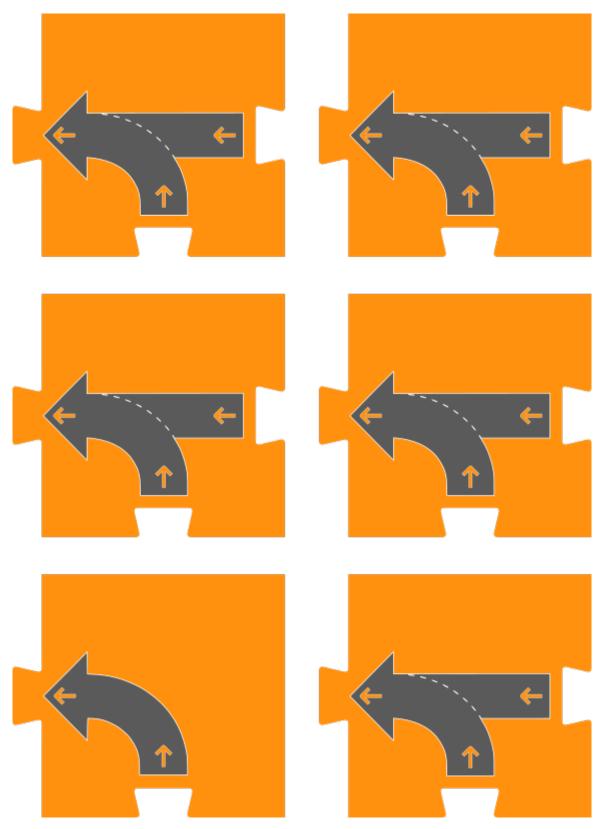




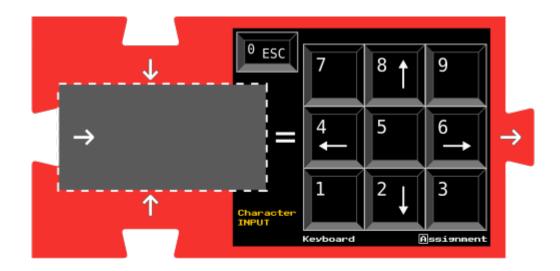
V	V

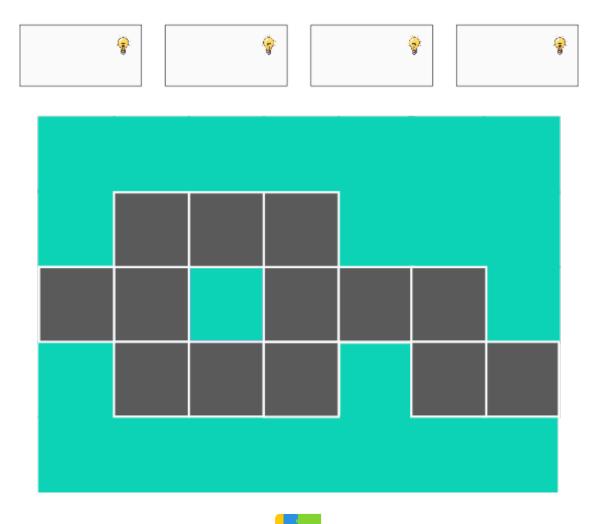












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